

**AMENDMENTS TO THE CLAIMS**

1. (Previously Presented) A circuit to access unstored data comprising:  
a register file;  
a first bi-directional OR controller connected to said register file;  
a first multiplexer having a first input connected to an output of said first bi-directional OR controller;  
a second bi-directional OR controller connected to said register file;  
a second multiplexer having a first input connected to an output of said second bi-directional OR controller; and  
a control circuit connected to said first bi-directional OR controller and said second bi-directional OR controller.
2. (Previously Presented) The circuit according to claim 1 further comprising:  
a first latch having an output connected to a first input of said first bi-directional OR controller; and  
a second latch having an output connected to a first input to said second bi-directional OR controller.
3. (Previously Presented) The circuit according to claim 2 further comprising:  
a first functional unit having a first output connected to a second input to said first multiplexer and having a second output connected to an input to said first latch.
4. (Previously Presented) The circuit according to claim 3 further comprising:  
a second functional unit having a first output connected to a second input of said second multiplexer and having a second output connected to an input of said second latch.
5. (Previously Presented) The circuit according to claim 4 wherein:  
said first multiplexer is part of a first data forwarding circuit; and  
said second multiplexer is part of a second data forwarding circuit.
6. – 20. (Canceled)